

# target triples

---

lies, damned lies, and target triples

target = "machine code can run on"

target triple = 3 components

```
>_ zsh X + default Mon Oct 28 10:46 souffle.local

~ via 🦆 v3.9.6
> rustc -vV
rustc 1.82.0 (f6e511eec 2024-10-15)
binary: rustc
commit-hash: f6e511eec7342f59a25f7c0534f1d01b14
commit-date: 2024-10-15
host: aarch64-apple-darwin
release: 1.82.0
LLVM version: 19.1.1

🦆 v3.9.6
```



**aarch64** - apple - darwin

aarch64 - **apple** - darwin

aarch64-apple-**darwin**

LEANDER KAHNEY

SCIENCE MAR 16, 1999 4:15 PM

# Apple Opens OS Code

**WIRED**

"It's about evolution," said Apple's interim CEO, Steve Jobs, at an unveiling at Apple's campus in Cupertino, California. "We believe that putting it out there ... will help us make it the best technology -- collectively -- we know how to make."



**x86\_64**-apple-darwin



**powerpc** - unknown - linux - gnu  
**powerpc64** - unknown - linux - gnu  
**powerpc64le** - unknown - linux - gnu



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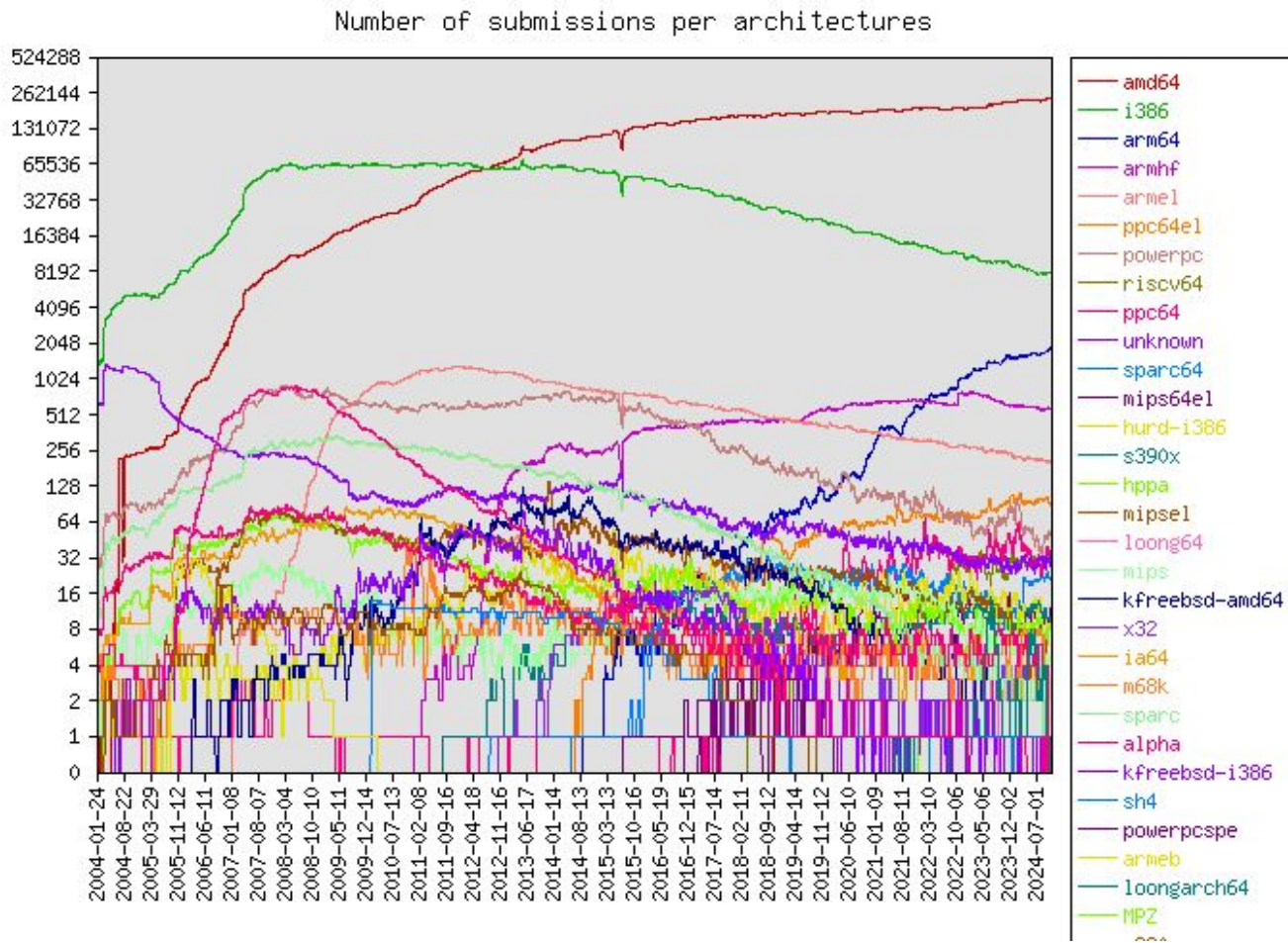
TL2SV3

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Image Coming Soon





**why** is powerpc64[le] tier 2?

# "Legacy" tier 2 targets have misplaced or absent maintainer docs #113739

 Open



workingjubilee opened on Jul 16, 2023

edited by RalfJung · Edits · ...

## Location

This affects our [platform support documentation](#).

It specifically affects targets added before the [target tier policy](#) was confirmed, and especially those that are tier 2.

## Summary

It is widely expected that the existing tier 1 targets are of primary concern for the Rust Project in general. As I understand it, the current absence of formally documented maintainers for them is based on the belief we have a large enough surplus of "target maintainers" for them that we can expect these targets to be effectively supported by "whoever picks up the slack".

However, tier 2 targets are trickier. Many are more niche, harder to find and run code on, and require specialized developer knowledge. These realities are part of *why* we expect targets to have target maintainers. Yet we have several without any documented support because they predate the target tier policy. This has recently led to us being forced to respond to [exigent circumstances](#) by [interrupting our usual support](#) because they implicitly violated the other side of the target support "contract": they impeded development of all the

### Assignees

No one assigned

### Labels



### Type

No type

### Projects

No projects

### Milestone

No milestone

**powerpc** - unknown - linux - gnu  
**powerpc64** - unknown - linux - gnu  
**powerpc64le** - unknown - linux - gnu

x86\_64-unknown-linux-**gnu**


x86\_64-unknown-linux-**musl**

>\_ zsh X +

default

Mon Oct 28 12:47

souffle.local

amos in  hell in ~/musl-samples

> ldd ./cargo-dist-x86\_64-unknown-linux-{gnu,musl}/cargo-dist

./cargo-dist-x86\_64-unknown-linux-gnu/cargo-dist:

linux-vdso.so.1 (0x00007ffea1bdb000)

libgcc\_s.so.1 ⇒ /lib/x86\_64-linux-gnu/libgcc\_s.so.1 (0x00007f88f805a000)

libpthread.so.0 ⇒ /lib/x86\_64-linux-gnu/libpthread.so.0 (0x00007f88f8055000)

libm.so.6 ⇒ /lib/x86\_64-linux-gnu/libm.so.6 (0x00007f88f7f76000)


libdl.so.2 ⇒ /lib/x86\_64-linux-gnu/libdl.so.2 (0x00007f88f7f71000)

libc.so.6 ⇒ /lib/x86\_64-linux-gnu/libc.so.6 (0x00007f88f6c1f000)

/lib64/ld-linux-x86-64.so.2 (0x00007f88f8081000)

./cargo-dist-x86\_64-unknown-linux-musl/cargo-dist:

statically linked

amos in  hell in ~/musl-samples

> |

# Update the existing musl targets to be dynamically linked. #422

New issue 

 Closed

 **pnkfelix** opened on Apr 1, 2021 edited by estebank · Edits ▾ ...

## Update the existing musl targets to be dynamically linked.

We should change the `-musl` targets to be consistently dynamically linked to their `libc`, rather than statically linking the `musl` `libc` into them at compile-time. If a MUSL end-user wants the `musl-libc` to be statically-linked into their program, they will need to specify it explicitly (e.g. via `-Ctarget-feature=+crt-static`)

Assignees  
No one assigned

Labels  
`T-compiler` `major-change`  
`major-change-accepted`

`-Ctarget-feature=+crt-static`

**i686** - pc - windows - gnu

**i**5**86** - pc - windows - gnu



x86\_64-**uwp** -windows-**gnu**

x86\_64-**uwp** -windows-msvc

x86\_64-**win7**-windows-msvc

**aarch64**-apple-darwin

**aarch64**-unknown-linux-gnu

**aarch64**-pc-windows-msvc

**arm** = ARMv6

**armv7** = ARMv7

**aarch64** = ARMv8

armv7a

armv7r

arめbv7r

thumb**v6m**

thumb**v7m**

thumb**v7em**

thumb**v8m.base**

thumb**v8m.main**

**arm64\_32** - apple - watchos

aarch64\_be-unknown-linux-**gnu\_ilp32**

**m68k** - unknown - linux - gnu



**loongarch64-unknown-linux-\***

aarch64-**fuchsia**  
x86\_64-**fuchsia**

aarch64-unknown-**uefi**

aarch64 - unknown - **redox**

aarch64-nintendo-switch-**freestanding**

armv6k - **nintendo-3ds**

armv7 - **sony-vita** - newlibeabihf

arm-unknown-linux-gnueabi

arm-unknown-linux-gnueabihf

**nvptx64**-nvidia-cuda



**s390x** - unknown - linux - gnu

**wasm32-unknown-unknown**

**wasm32-wasi**

arm64\_32-apple-watchos

aarch64-apple-watchos

**aarch64-apple-watchos-sim**

**x86\_64-apple-watchos-sim**

x86\_64-apple-ios-macabi

